JC #1 B

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Yoshiaki YAMAMOTO

Serial No. 09/089,666

Filed June 3, 1998

BOX ISSUE FEE

Allowed May 25, 2000

BATCH N06

METHOD OF FABRICATING SEMICONDUCTOR DEVICE FOR PREVENTING RISING-UP OF SILISIDE

SUBMISSION OF FORMAL DRAWINGS

Commissioner for Patents

Washington, D.C. 20231

Sir:

In response to PTOL-37 mailed May 25, 2000, replace the drawings originally filed with the accompanying new formal drawings.

Respectfully submitted,

YOUNG & THOMPSON

By _____

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August 7, 2000

05.00